FIG.1

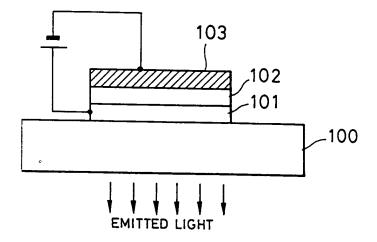


FIG.2

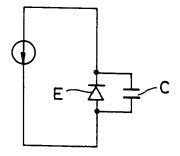
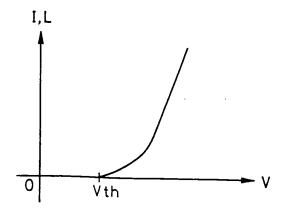


FIG.3



2mB AmB 6mB~ 汉 EmB,2 EmB,3 EmB,n EmB,1 AmG 6mg~ EmG,2 EmG,3 EmG, 1 EmGn 7mR AmR 來 6mR~ EmR,3 EmR,n EmR,2 EmR,1 **2**2B 62B ~ A2B E28,2 E2B,3 E28,n E2B,1 A2G 626~ E2G,n E26,2 E2G,3 E2G,1 62R~ A2R 218 E2R,3 E2R,n E2R,2 618 7 A1B E18,3 E18,2 E1B,n 61G-7 A1G E16,n 5/5/ E16,3 E16,1 E16,2 GIR Y A1R E1R,3 E1R_n E1R,2 3) **B**2 **B**3 В <u>8</u> 2 Vcc 5₂ Vcc 53 V_CC 5₁ LIGHT EMISSION CONTROL CIRCUIT

F16.4

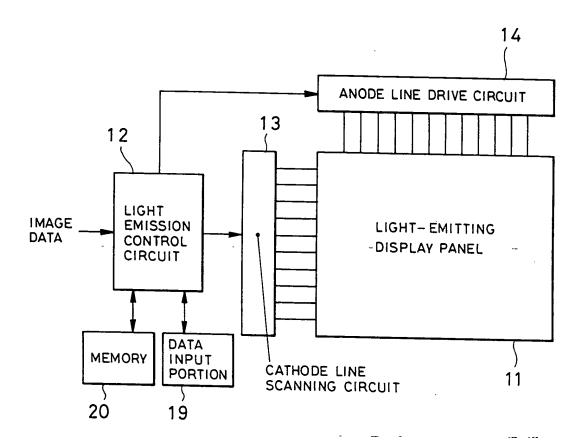
2mB AmB 6mB~ EmB₂2 EmB,1 EmB,3 EmB,n 7mG 6mG~ Amg EmG,1 EmG,2 EmG₃ EmG,n 7 mR AmR 6mR~ EmR,1 EmR,2 EmR,n EmR,3 **2**2B 62B ~ A2B -22G E28,2 E28.1 E28,n E283 Azg 62G~\ E2G,2 E26,3 E₂G,n E26,1 A2R 62R~ -218 E2R,1 E2R,2 E2R,3 E2R,n 618~ A1B -21G E18,1 E18,2 E18,3 E18,n 616~ A1G **21R** E16,1 E16,2 E16,3 E1G,n 61R~ A1R E1R,1 E1R,2 E1R,3 E1R,n 3 **B**2 <u>6</u> \mathbf{B}_3 쪞 7 Vcc 52 Vcc 53 Vcc 51 39 Vcc | 5_n LIGHT EMISSION CONTROL CIRCUIT

F16.5

2mB AmB 6mB~ EmB,1 EmB,2 EmB,3 EmB,n 6mR~\ 6mG~\ Amg EmG,2 ▼EmG,n [†]EmG,3 EmG,1 7 mR AmR EmR,2 EmR,3 EmR,1 EmR,n -22B \$ A2B 628∼ E28,n E2B,2 E2B,3 E2B,1 62G-4 A2G -22R E2G.2 E2G,3 /**E**2G,n E2G,1 72R 62R~ A2R -218 |¶ E2R,n E2R,2 E2R,1 E2R,3 618~ A1B -21G E18,n E1B,1 E18,2 E18,3 616~ A1G 21R E16,1 E16,2 E16,n E16,3 A1R 61R~ E1R,1 E1R,3 E1R,2 E1R,n ന <u>B</u> **B**2 **B**3 Б Λ_cc 5₃ Vcc 511 2 Vcc 52 Vcc 5n LIGHT EMISSION CONTROL CIRCUIT IMAGE DATA

F16.6

FIG.7



AmB EmB,2 EmB_{,3} EmB,1 EmB.n AmG EmG,1 EmG,2 EmG,3 EmG,n AmR EmR,1 EmR,2 EmR,3 EmR,n 162B A2B E2B,2 E28,3 E2B,n E28,1 162G A2G E2G,1 E2G,2 E26,3 E2G,n 162R A2R E2R,1 E2R,3 E2R,2 E2R.n 161B A1B E18,1 E18,3 E18,2 E18,n 161G A1G E16,1 E16,3 E16,2 E1G,n 161R A1R E1R,1 181R E1R,3 E1R,2 - E1R,n ā **B**2 **B**3 Вп 1/cc151 4 DATA INPUT PORTION တ 12 LIGHT EMISSION CONTROL CIRCUIT MEMORY 20

FIG.8

FIG.9

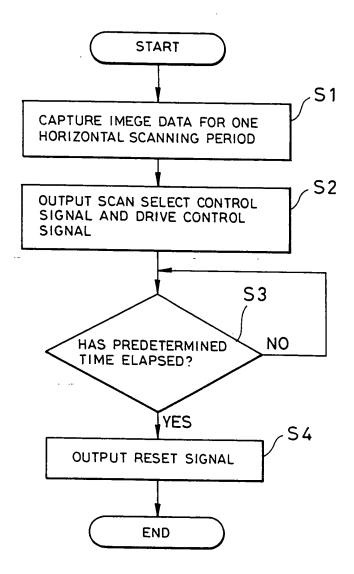
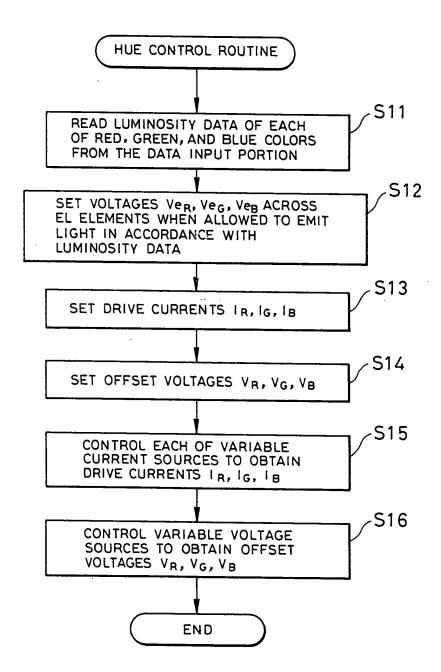


FIG.10



AmB EmB,2 EmB,1 EmB,3 EmB,n Amg EmG,1 EmG,2 EmG,3 EmG.n AmR EmR,2 EmR,1 EmR,3 EmR,n 162B A2B E28,2 E2B,3 E2B,1 E_{2B,n} A2G E2G,1 E2G,2 E26,3 E2G,n 162R A2R E2R,1 E2R,3 E2R,2 E2R,n 16_{1B} A1B E18,3 E18,1 E18,2 **E**18,n 161G A1G E16,3 E16,2 E16,n E16,1 161R A1R E1R,3 | **E**1R,n E1R,2 **B**2 <u>a</u> **B**3 Vcc153 DATA INPUT PORTION 7 LIGHT EMISSION CONTROL CIRCUIT MEMORY

찙

⁄ુ≎⊭

19

20

F16.11

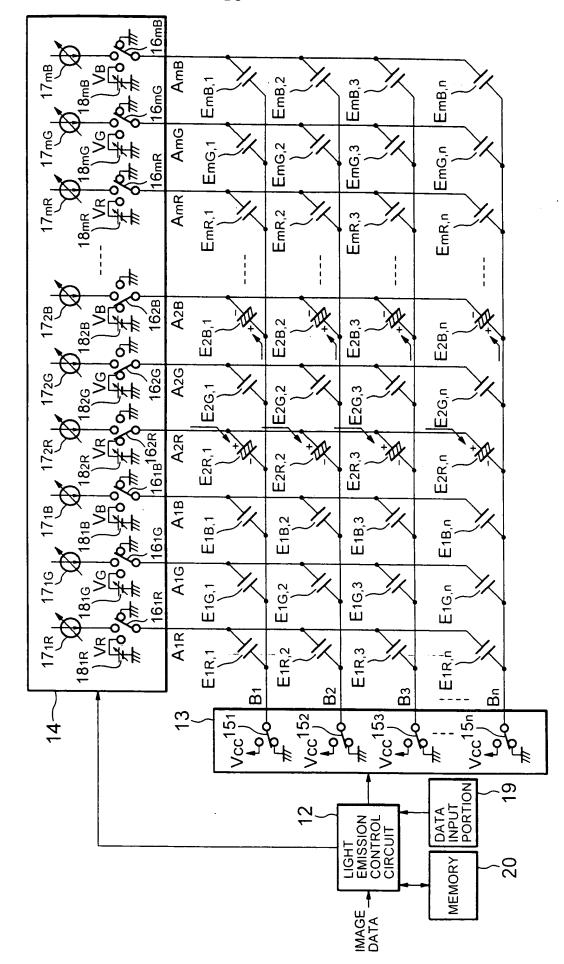


FIG.12

F16.13

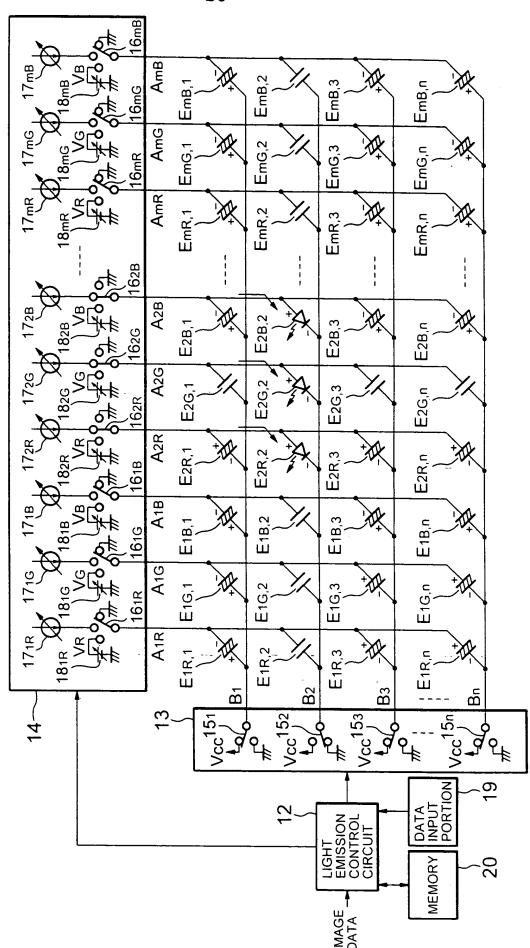


FIG.14A

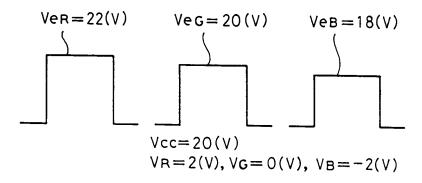
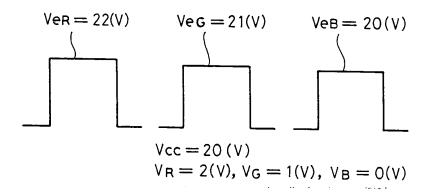
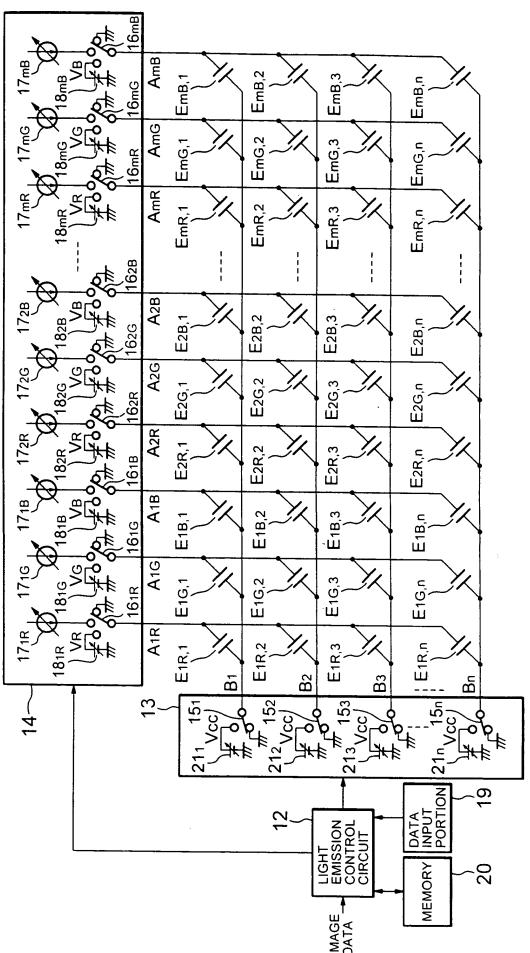


FIG.14B





F16.15

FIG.16

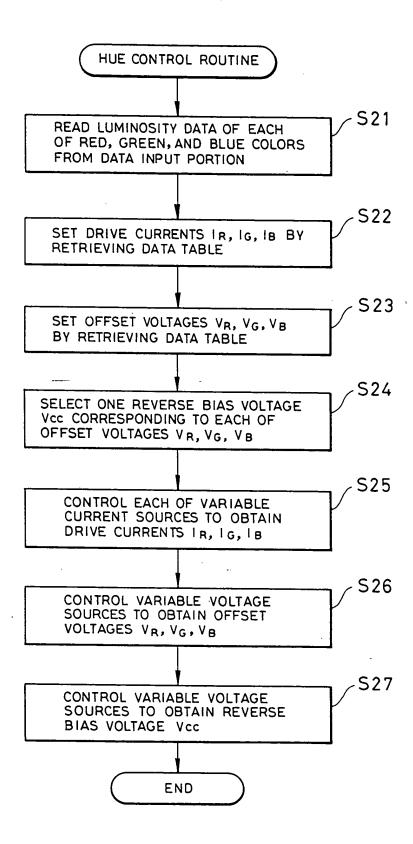


FIG.17

| LEVELS OF HALFTONE | | 0 | 1 | 2 | 29 | 30 | 31 |
|--------------------|----------------|------|-------|------|-----------|--------|------|
| RED | DRIVE CURRENT | lr0 | Ir1 | Ir2 | Ir29 | 1'r30 | Ir31 |
| | OFFSET VOLTAGE | VrO | V r 1 | Vr2 | Vr29 | Vr30 | Vr31 |
| GREEN | DRIVE CURRENT | Ig-0 | lg1 | lg 2 | lg 29 | lg 3 0 | lg31 |
| | OFFSET VOLTAGE | Vg0 | Vg1 | Vg2 | Vg29 | Vg30 | Vg31 |
| BLUE | DRIVE CURRENT | IP0 | 1Ь1 | 162 | 1629 | Ib30 | 1631 |
| | OFFSET VOLTAGE | VÞO | V b 1 | Vb2 | Vb29 | Vb30 | Vb31 |

FIG.18A

VeR = 25(V)

| | T | , , | | | | | | | |
|-----------------------------------|----|-----|----|----|----|----|----|----|----|
| OFFSET VOLTAGE V _R (V) | -5 | -4 | -3 | -2 | -1 | 0 | +1 | +2 | +3 |
| · REVERSE BIAS VOLTAGE Vcc (V) | 30 | 29 | 28 | 27 | 26 | 25 | 24 | 23 | 22 |

COMMON VOLTAGE RANGE

FIG.18B

VeG = 21(V)

| OFFSET VOLTAGE V _G (V) | -5 | -4 | -3 | -2 | -1 | 0 | +1 | +2 | +3 |
|-----------------------------------|----|----|----|----|----|----|----|-----|----|
| REVERSE BIAS VOLTAGE Vcc (V) | 26 | 25 | 24 | 23 | 22 | 21 | 20 | 1.9 | 18 |

COMMON VOLTAGE RANGE

FIG.18C

VeB = 20(V)

| OFFSET VOLTAGE VB (V) | - 5 | -4 | -3 | -2 | -1 | 0 | +1 | +2 | +3 |
|---------------------------------|------------|----|----|----|----|----|----|----|----|
| REVERSE BIAS VOLTAGE Vcc (V) | 25 | 24 | 23 | 22 | 21 | 20 | 19 | 18 | 17 |

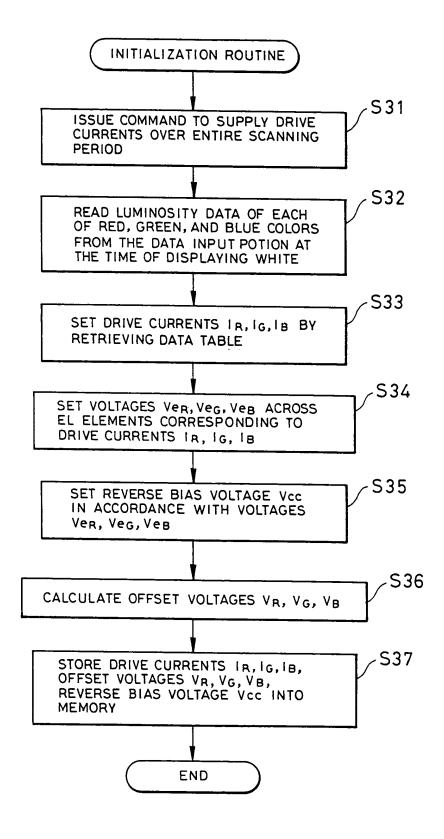
COMMON VOLTAGE RANGE

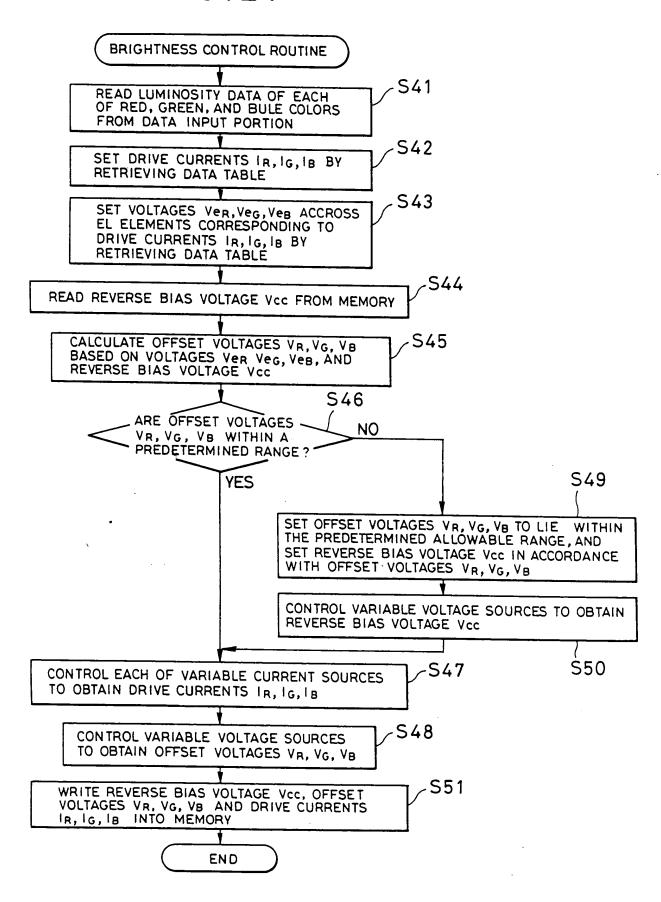
FIG.19

VeR = 9(V)

| | | | | | | | | ٠, | |
|-----------------------------------|----|----|----|----|----|---|----|----|----|
| OFFSET VOLTAGE V _R (V) | -5 | -4 | -3 | -2 | -1 | 0 | +1 | +2 | +3 |
| REVERSE BIAS VOLTAGE Vcc (V) | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 |

FIG.20





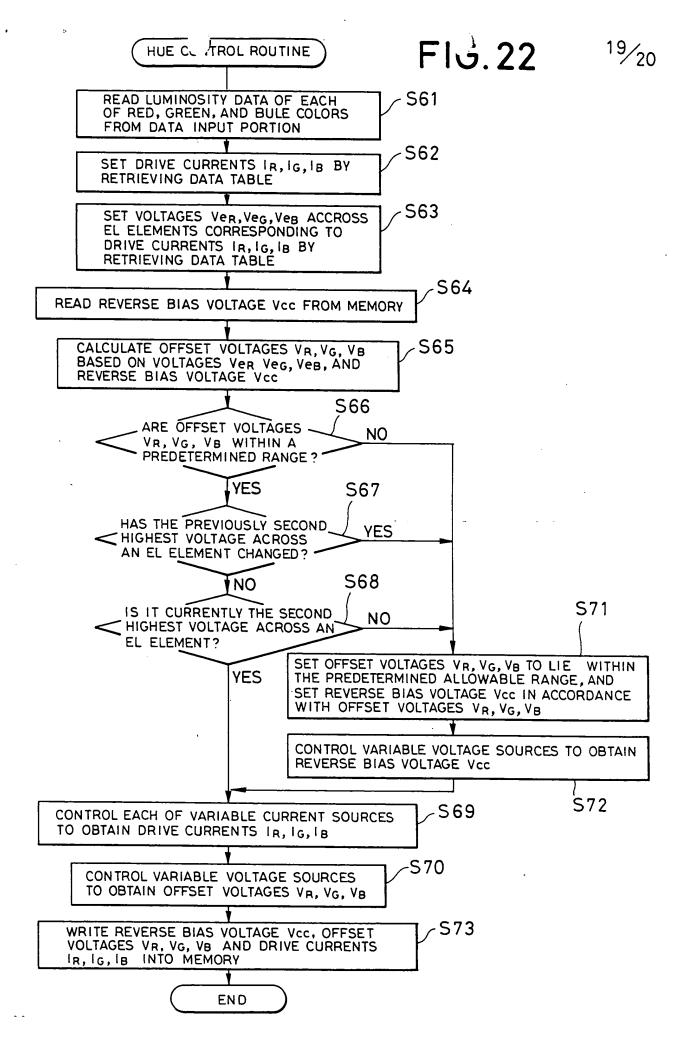


FIG.23

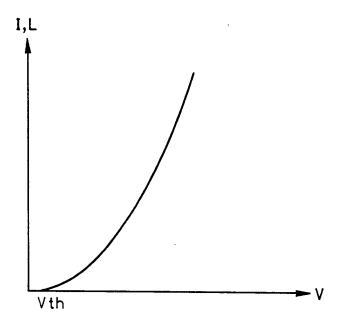


FIG. 24

